

IN THE CLAIMS:

Please cancel Claims 3 and 8 without prejudice to or disclaimer of the subject matter recited therein.

Please amend Claims 1, 5, 7 and 10 to read as follows. Note that all claims currently pending in this application are being reproduced below for the Examiner's convenience.

1. (Currently Amended) An electronic apparatus comprising:  
a clock circuit that generates a clock signal having clock signal pulses generated at a predetermined cycle; ~~and~~  
a non-volatile memory time-keeping counter circuit; and  
a control circuit that controls said electronic apparatus,  
wherein the clock signal pulses generated by said clock circuit are input to said control circuit, and said control circuit outputs the clock signal pulses to said non-volatile memory time-keeping circuit so as to count the ~~for counting~~ clock signal pulses generated by said clock circuit and ~~store~~ ~~storing~~ a count of the clock signal pulses in said non-volatile memory time-keeping counter circuit.

2. (Previously Amended) An electronic apparatus according to Claim 1, wherein said non-volatile memory time-keeping counter circuit comprises a ferroelectric memory.

Claim 3 (Cancelled).

4. (Original) An electronic apparatus according to Claim 1, wherein said electronic apparatus comprises a camera.

5. (Currently Amended) An electronic apparatus according to ~~Claim 4~~ Claim 1, ~~further comprising a control circuit that controls said electronic apparatus, and wherein~~ said control circuit includes a central processing unit.

6. (Previously Amended) An electronic apparatus according to Claim 1, wherein, when a power supply battery for supplying power to said electronic apparatus is replaced, said non-volatile memory time-keeping counter circuit starts counting in a state in which a predetermined value is added to the memory contents of said non-volatile memory time-keeping counter circuit.

7. (Currently Amended) An electronic apparatus comprising:  
a time-keeping clock signal generation circuit that keeps time; ~~and~~  
a ferroelectric memory time-keeping counter circuit; and  
a control circuit that controls said electronic apparatus,  
wherein the time kept by said time-keeping clock signal generation circuit is  
input to said control circuit, and said control circuit outputs the time to said ferroelectric memory  
time-keeping counter circuit such that said ferroelectric memory time-keeping counter circuit that

forms and stores a time signal concerning time kept by said time-keeping clock signal generation circuit.

Claim 8 (Cancelled).

9. (Original) An electronic apparatus according to Claim 7, wherein said electronic apparatus comprises a camera.

10. (Currently Amended) An electronic apparatus according to Claim 7, ~~further comprising a control circuit that controls said electronic apparatus, and~~ wherein said control circuit includes a central processing unit.